Computer architecture

Aim;- 2 bit half adder

Diagram:-

|  |
| --- |
|  |

Truth table:-

|  |  |  |  |
| --- | --- | --- | --- |
| A | B | SUM | CARRY |
| 0 | 0 | 0 | 0 |
| 0 | 1 | 1 | 0 |
| 1 | 0 | 1 | 0 |
| 1 | 1 | 0 | 1 |